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				Examiner Name PHAM, Hoai V.								
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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.





Attorney Docket No. MTI-31269

N THE LINE ED STATES PATENT AND TRADEMARK OFFICE

Applicants

Yates, et al.

Serial No.

10/050,390

Filing Date

January 16, 2002

Examiner

PHAM, Hoai V.

Group Art Unit:

2811

For

Method for Enhancing Electrode Surface Area in DRAM Cell Capacitors

Confirmation No.:

6193

CERTIFICATION UNDER 37 CFR 1.8(a) and 1.10

I hereby certify that, on the date shown below, this correspondence is being deposited with the United States Postal Service in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231 as "Express Mail Post Office to Addressee" Mailing Label No. EV 405077965 US.

Date: Ja

Jan. 20, 2004

antonia J. Otto

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

RESPONSE

Sir:

Applicant requests consideration of the pending claims in the above-identified patent application based on the remarks herein.

Amendments to the Abstract begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of the claims, which begins on page 3 of this paper.

Remarks begin on page 12 of this paper.

JAN 28 2004